

Final study guide -

→ "cheat sheet" - 1 page 8 1/2 x 11 both sides.

Circuits:

Common source, common emitter — [Class A.
Resistor load.
Emitter/source degeneration
biasing
Current mirror load.

Source/emitter follower

Current mirrors

FET as resistor — [Class A, AB, B
single ended - resistor
current mirror
push-pull / complementary

Multistage.

Differential amps, op-amps

Design

Analysis

Filters.

LP, HP from specs.

Scaling a prototype

Transfer function. (derive, understand)

Analysis (determine transfer function from circuit)

Design