

Where we stand --

(4A)
1

Chapter 4 Sec 1-5, with forward references. - Done

6, 7 are Electronics I

except body effect } DO
Need design example. } EXERCISES

8, 9 - defer

10 Electronics I

11 - is easy - do exercises.

12 - done some all along - read it.

Chapter 5 - BJT corresponding topics -
same ideas - use as reference.

Chapter 6 - IC'S - this week, less detail

Sec 1 - Design philosophy - done

2 - Mos vs. BJT - Ties it together -

do exercises

→ 3 - Current sources - started in Ch. 4 - more detail

4 - High frequency - defer.

→ 5 - Active loads - started in Ch. 4 - more detail.

6 - High frequency - defer

7 - Common gate + common base. with active loads.
won't cover

8 - Cascode - won't cover

9 - Source/emitter degeneration - more detail -
done enough.

→ 10 Source + emitter followers - (?)

→ 11 Pairings - (?)

12 Improved current mirror - defer.

13 - Spice - What are they covering here?

Chapter 7 - Differential Amps.

1 - Mos differential pair - did BJT do exercises
in less detail

2 - Small signal operation - do exercises

3 - BJT do exercises

4 - Non-ideal characteristics -
read - exposure.

→ 5 - diff-pair with active load.

6 - Freq resp. - defer.

→ 7 - multistage - will cover

8 - Spice - ??