

# Ch. 3 - Transistor theory

## 3.1 - Basic theory

NPN, PNP

Curves

~~leakage, breakdown~~

Today

## 3.2 DC Analysis

Common emitter

Load line

other circuits.

Friday

## 3.3 Basic transistor applications.

Switch

Digital logic

Amplifier

## 3.4 Biasing

single resistor

VD Hoge divider

Current mirrors.

Monday  
(WK 8)

wed = review  
Fri = Lab

## 3.5 Multi-stage

# Ch 4 - Transistor amplifiers

## 4.1 Analog signals

Monday  
(WK 9)

## 4.2 Linear amplifier

graphical analysis

small signal models

## 4.3 Basic amplifier configurations

## 4.4 Common emitter Amplifier

Wed.

## 4.5 AC load line analysis

Fri

## 4.6 Common collector (emitter follower)

Monday  
(WK 10)

## 4.7 Common base

wed

## 4.8 Summary

Fri

## 4.9 Multistage

Monday WK 11

Cascade

Cascade

## 4.10 Power considerations.

wed

→ Test Fri WK 11.

## HW - Ch. 3 -

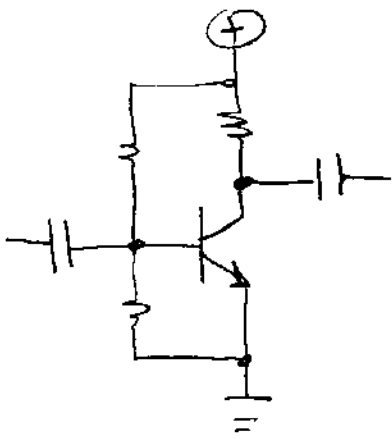
### Exercises

	P	#	P	#
Sec 3.1	104	1, 2	152	1, 4, 7, <del>10</del> , 14
	107	3, 4	153	14, 16
	110	5, 6		
	<del>113</del>	<del>7, 8</del>		
	121	9, 10		

Not to hand in.

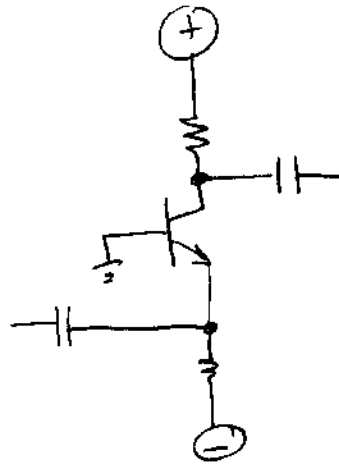
## 3 types of amplifiers

7B  
2



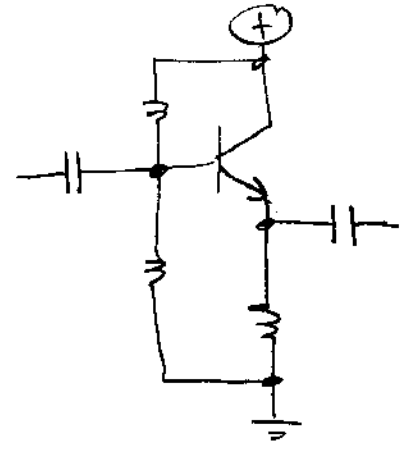
Common emitter

High voltage gain  
moderate current gain



Common base

High voltage gain  
Current gain  $< 1$



Common collector

High current gain  
Voltage gain  $< 1$

(just a preview. we need to bias them first.)

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Lab reports -

general comments -

All are OK.

Label the plots -

I could not tell what they mean

Mix text and figures -

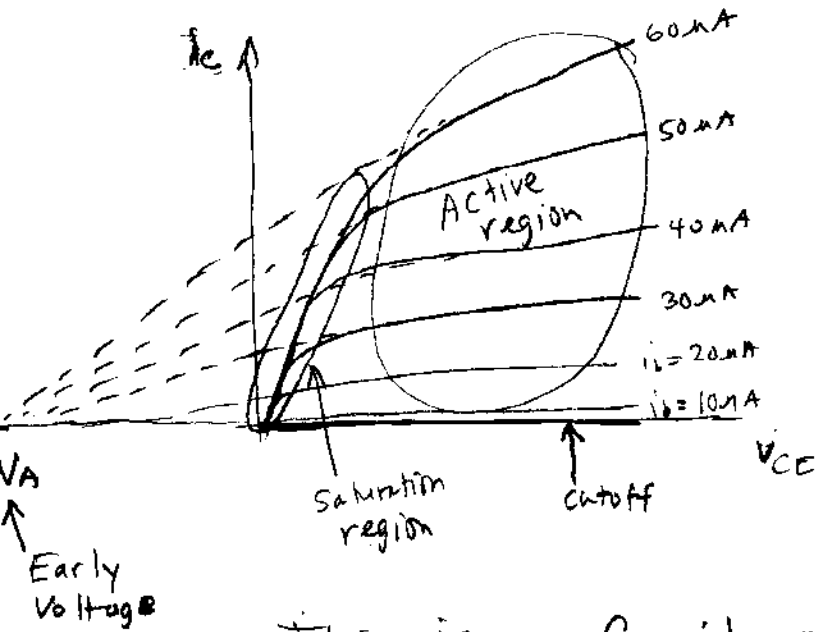
(as opposed to Fig 1, Fig 2, etc)

Write the notes on the same page as the scope picture.

Procedure needs more detail -

Someone else should be able to repeat the experiment, with all the mistakes.

# Transistor characteristic curves.

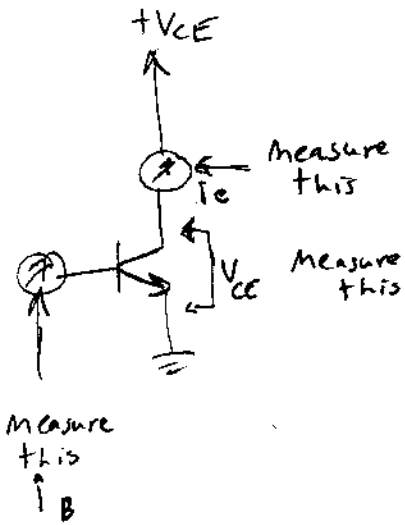


3 regions - Active  
Saturation  
Cutoff.

This is a family of curves.

"Common emitter characteristics" -

One curve for each sample  $V_{BE}$ .



Hold  $i_B$  constant -  
Sweep  $V_C$   
Measure  $i_C$

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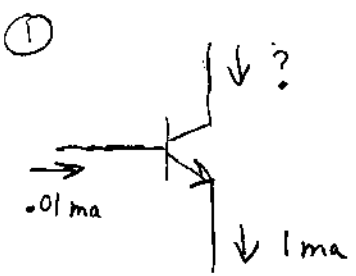
## Breakdown - (3.1.6)

Like a zener diode -

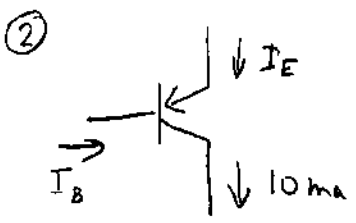
When the voltage is too high, it breaks down -

non-destructive, but current can be destructive.

Examples



What is  $\beta$ ?  
 $\alpha$ ?  
 $I_C$ ?

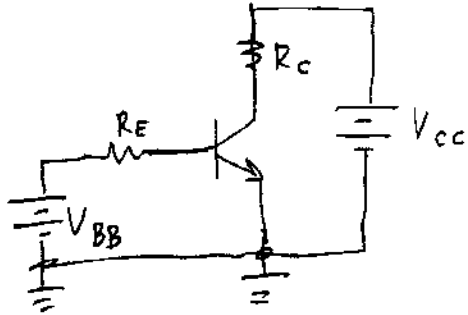


$\beta = 100$   
What is  $I_B$ ?  
 $I_E$ ?

## 3.2 DC Analysis of Transistor Circuits

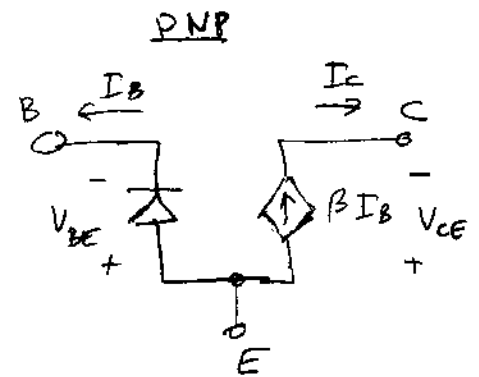
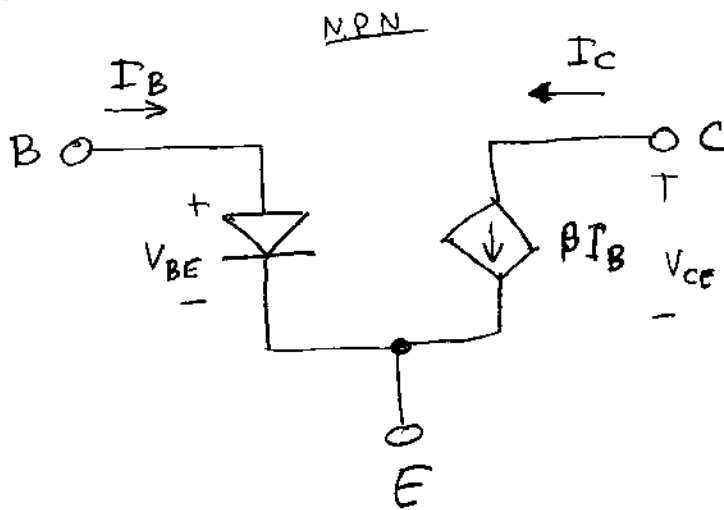
7B  
5

### 3.2.1 Common Emitter circuit.



What are voltages and currents?

Equivalent circuit of transistor: (Model)

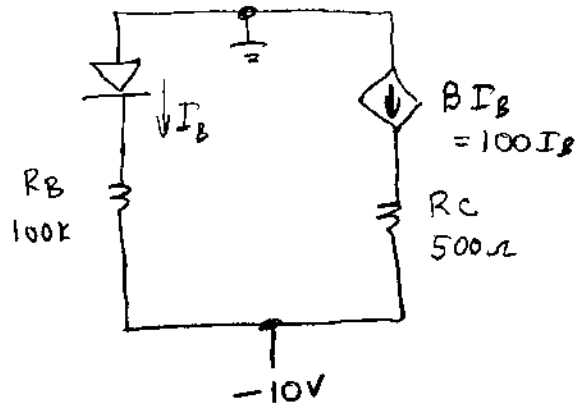
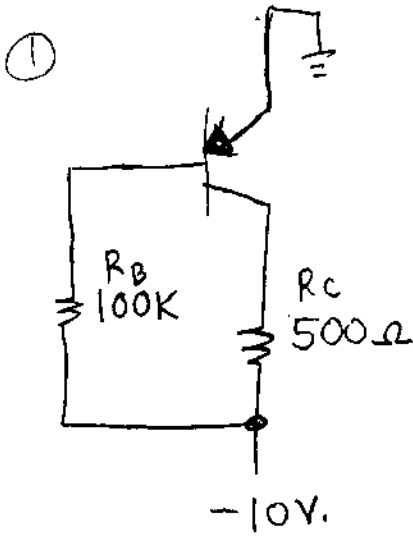


Substitute the model for the transistor, and analyze.

Example:

Transistor parameters:

$V_{BE} = .6$       $\beta = 100$



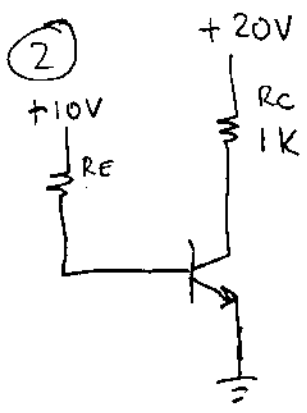
$V_{RB} = 9.4$       $I_B = \frac{9.4}{100K} = .094 \text{ ma}$

$I_C = \beta I_B = 9.4 \text{ ma}$

$V_{RC} = I_C R = (9.4 \text{ ma})(500) = 4.7$

$V_{CE} = (-10) - (-4.7) = -5.3 \text{ V}$

HW!  
Do again  
for  
 $\beta = 50, 200, 500$



$V_{BE} = .6$       $\beta = 100$

What is  $R_E$  for  $V_{CE} = 10$ ?