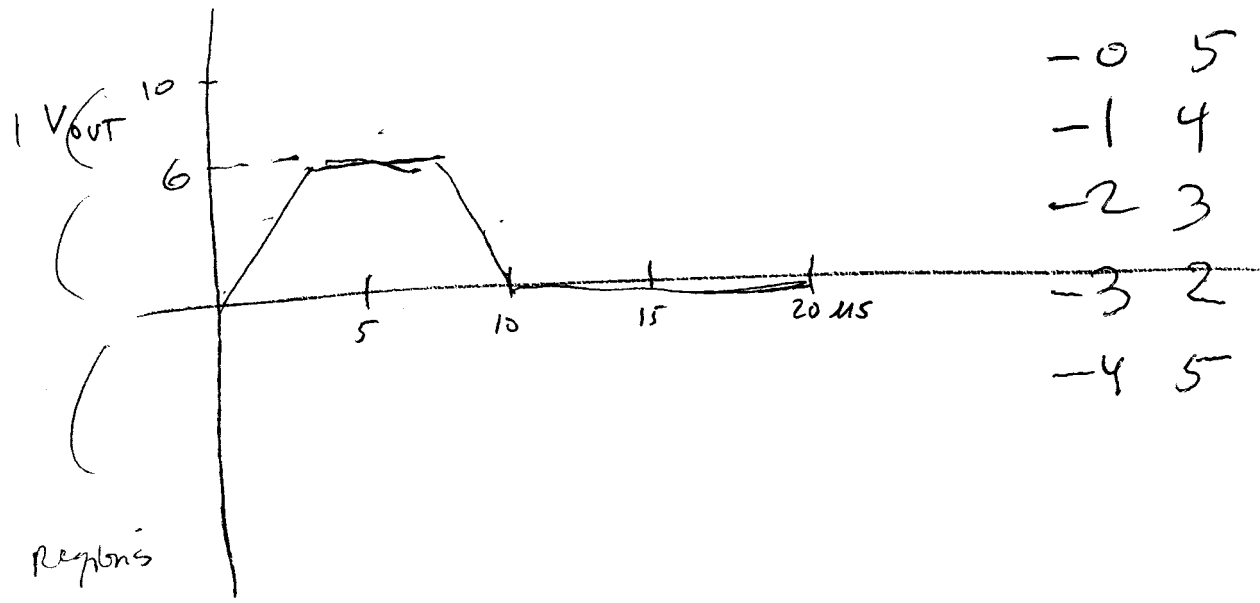
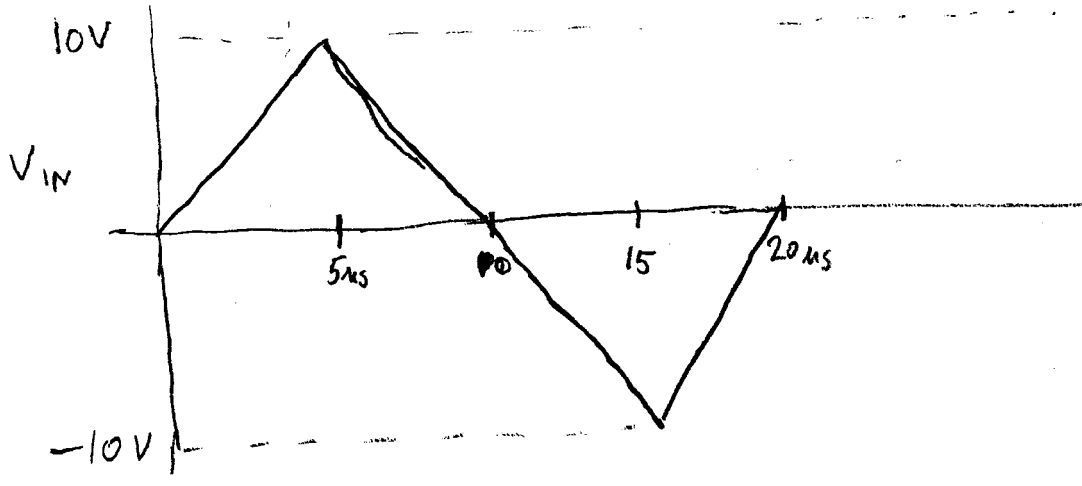
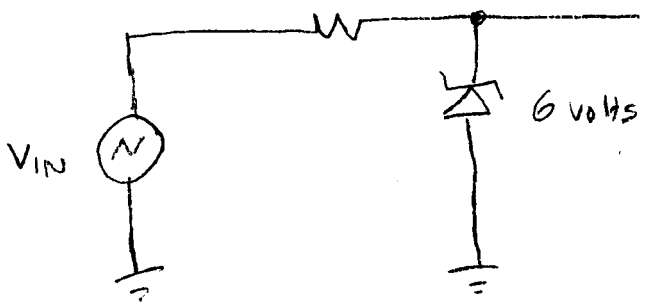


Name _____ Key **10**

V_{out}



-0	5
-1	4
-2	3
-3	2
-4	5

(3 were the reg dist point)

Regions

if kept - believable for passive circuit.

